							Form Num.			UIRL/F/84	
		PUSAT PENGURUSAN MAKMAL UNIVERSITI (PPMU)				Version			1/2024		
						Effective Date			01/03/2024		
UNIVERSITI TEKNOLOGI MALAYSIA	U					Equipment			PCB PLOTTER		
							Sample Serial No. UIF			UIRL/	
MICRONANO FABRICATION AND MACHINING LABORATORY											
SAMPLE SUBMISSION FORM											
 General Rules and Requirements: All information provided should be true Booking will be notified/updated by email Booking procedure Complete the application form including valid research vote number Submit the completed application form to UIRL Sample Acceptance Counter Fast Lane is offered to non-UTM customers with an additional 50% charge from the normal price Sample Condition & Preparation PPMU has the right to cancel any analysis if the sample is suspected to have a high risk on the safety of the operator or can cause damage to the instrument during the analysis. The cost of damages will be borne by the customer. The remaining samples will be disposed of within a month after analysis is completed. All enquiries regarding PCB PLOTTER should be forwarded to the Assistant Engineer, Mr Muhammad Sulaiman Muhammad Zain, email: m.sulaiman@utm.my, tel: 07-5557729 or visit our website at ppmu.utm.my. 											
1. APPLICANT'S PERSONAL PAR	TICULARS										
Name of Applicant	<u> </u>								<u> </u>		
Status of Applicant		ndergraduates		Maste	r		PhD			Research	
Student Matric No.											
Faculty/ Department											
Hand Phone No. & Email			_	_	_	_	_	_			
2. SUPERVISOR DETAILS (for inte	rnal applicant d	and academic i	institution	only)							
Name of Supervisor											
Staff ID No.											
Faculty/Department											
Hand Phone No.											
Email											
Mode of Payment	Ca	ash	EFT		Log ca	rd	In	voice		Fast Lane	
*Payment using invoice		Research Vot No. (e.g.: Q.J091600.24C3.01D32)									
		Balance of V29000 *A digital signature is not recommended. Any matters raised in the future are beyond our responsibilities									
Signature & Official Stamp	*A digital	signature is not red	commended	l. Any ma	itters raise	d in the fu	ture are b	eyond our r	esponsi	bilities	
3. SAMPLE & ANALYSIS INFORM	IATION										
Total No. of Sample/Model											
Design Format											
Model Material	Single L	ayer			Double	e Layer					
PCB Design Setting	PCB Size	e		/inc	Wid	th(norm	al 0.4)				/mm
	Time	Time		r/Min		Copies			/pcs		
Remarks	(etching i	(etching is not provided) (all pcb format should be in Gerber file)									